

NOTICE OF APPEAL FROM THE EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCE'S		Docket Number 8008-1052
Applicant(s): Yasutaka NAKASHIBA		Conf.: 2273
Application #: 10/812,282		Filed: March 30, 2004
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE		
Group Art Unit: 2815		Examiner: J. Jackson, Jr.

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the decision of the Examiner dated **January 30, 2008** rejecting the following claims: **1-4, 6-8, 13, 16 and 23-35.**

The fee for this Notice of Appeal is: **\$510.00**

If I have checked one of the boxes below, then this is also a petition for an extension of time under 37 CFR 1.136(a) to reply to the rejection:

Extension of time requested for:	Non-small entity fee for extension of time:
<input type="checkbox"/> - 1 Month	\$120.00
<input type="checkbox"/> - 2 Months	\$460.00
<input checked="" type="checkbox"/> - the third month	\$590.00
<input type="checkbox"/> - 4 Months (if available)	\$1,640.00

The sum of the fee for the Notice of Appeal and the fee for any extension of time is: **\$1100.00**

☐ Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee shown above is reduced by half, and the resulting fee is: **\$**

☐ A check in the amount of the fee is enclosed.

☐ The Commissioner has already been authorized to charge fees in this application to a Deposit Account. I have enclosed a duplicate copy of this sheet.

☒ The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to our credit card.

Respectfully submitted,

YOUNG & THOMPSON

/Thomas W. Perkins/
Thomas W. Perkins, Reg. No. 33,027
209 Madison Street, Suite 500
Alexandria, VA 22314
Telephone (703) 521-2297
Telefax (703) 685-0573
(703) 979-4709

TWP/lrs
July 29, 2008